

## Abstract

### Transceiver with integrated hybrid circuit

A transceiver for a transmission and reception signal which can be transmitted via a signal line having a particular line impedance ( $Z_{\text{LINE}}$ ), having a line driver (6) for driving a transmission signal via the signal line and having an analog echo cancellation filter (15) for signal suppression for an echo signal brought about by the transmission signal, where the line driver (6) has a synthesized output impedance ( $R_{\text{SYN}}$ ), with the line driver (6) having a downstream hybrid circuit (11) for connecting an analog echo cancellation filter (15).

Figure 2